

is the characteristic of the load circuit LCI. The load circuit LCI becomes large capacitance CD at one time and small capacitance at other times. The change of load capacitance is controlled by control signals ϕ_1 and ϕ_2 . When the load capacitance is large, the load driving ability of Q4 may be increased so as to charge the load circuit quickly.

line 16, insert // The boosting node 2 makes driving ability of Q4 larger. // before "Transistors" and delete "therefor are";

line 17, insert -- are provided for boosting the node 2 -- after "C₁ and C₂";

line 19, insert -- control signal -- after "the next".

Page 33, line 2, insert -- control signal -- after "boosted by" and insert -- control signal -- after "When";

line 17, delete "2" and insert -- 3 -- and delete "3" and insert -- 2 --.

IN THE CLAIMS:

Please amend the claims as follows:

7. (three times amended) A semiconductor integrated circuit comprising:

a chip;

first circuits provided on said chip;

second circuits provided on said chip; and